

1072 U.S. PTO
10/039045
12/31/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10039045	FILING DATE 12/31/2001	CLASS 711	SUBCLASS 152	GAU 2180	EXAMINER Li
----------------------	---------------------------	--------------	-----------------	-------------	----------------

111 3
**APPLICANTS: Vo Hahn;

**CONTINUING DATA VERIFIED:

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO H052617.1142US0
TITLE : Low latency lock for multiprocessor computer system			
U.S.DEP.T. OF COMM./PAT.& TM-PTO-435L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
		Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		Application Examiner
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right inside flap)